

Date of submission: 27th March 2019

To The Secretary Listing Department BSE Limited Department of Corporate Services Phiroze Jeejeebhoy Towers, Dalal Street, Mumbai – 400 001 Scrip Code – 539551	To The Secretary Listing Department National Stock Exchange of India Limited Exchange Plaza, Bandra Kurla Complex Mumbai – 400 050 Scrip Code- NH
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Dear Sir/Madam,

Sub: Appointment of Ms. Nivruti Rai (DIN: 01353079) as an Additional Director (Non-executive & Independent) of the Company pursuant to amended Regulation 17 of SEBI (Listing Obligations and Disclosure Requirements) Regulations, 2015

Pursuant to Regulation 30 of the SEBI (Listing Obligations and Disclosure Requirements) Regulations, 2015, we hereby inform you that upon recommendation made by the Nomination & Remuneration Committee, the Board of Directors of the Company, vide circular resolution passed today 27th March 2019, have appointed Ms. Nivruti Rai (DIN: 01353079) as an Additional Director with effect from 27th March 2019 and to hold office as such till the conclusion of the ensuing Annual General Meeting and in compliance with amended Regulation 17 of SEBI (Listing Obligations and Disclosure Requirements) Regulations, 2015 she has been appointed in the capacity of an Independent Director for a period of three years effective from 27th March 2019 subject to the approval of the members in the ensuing Annual General Meeting.

The details prescribed as per SEBI circular No. CIR/CFD/CMD/4/2015 dated September 9, 2015 with regard to the said appointment are as under:

1.	Reason for change	Appointment
2.	Date of Appointment	27 th March 2019
	Term of appointment	Appointed as an Additional Director to hold office till conclusion of ensuing Annual General Meeting and in the capacity as an Independent Director for a tenure of 3 years subject to approval of members in the ensuing Annual General Meeting.
3.	Brief Profile	Enclosed as Annexure A.
4.	Disclosure of relationship between Directors	Ms. Nivruti Rai is not related to any Director of the Company.

We request you to take this on your record and oblige.

For Narayana Hrudayalaya Limited

Sridhar S
Sridhar S

Group Company Secretary, Legal and Compliance Officer



Narayana Hrudayalaya Limited

(Previously Narayana Hrudayalaya Pvt. Ltd.) CIN: L85110KA2000PLC027487

Registered Office: 258/A, Bommasandra Industrial Area, Anekal Taluk, Bangalore 560099

Corporate Office: 261/A, 2nd Floor, Bommasandra Industrial Area, Anekal Taluk, Bangalore 560099

Tel: +91 80 7122 2222 Fax: +91 80 2783 2648. www.narayanahealth.org

Annexure A

Profile of Ms. Nivruti Rai



Nivruti Rai is Vice President in the Data Center Group (DCG) and Country Head Intel India at Intel Corporation. Based in Bengaluru, she provides overall engineering and business unit leadership and leads operations for the site, driving innovation, cross-group efficiencies and execution for engineering teams delivering global products and roadmaps. She also leads engagements with national and local governments and policymakers, as well as collaboration with ecosystem players to enable innovation and entrepreneurship.

Nivruti is also responsible for DCG India site engineering and execution, with a focus on delivering platforms aligned with the India team's charter. In addition, she supports DCG and the Intel Sales and Marketing Group in strengthening customer, ecosystem, academic and research lab relationships in India.

She joined Intel in 1995 and subsequently worked in the CPU development organization in Oregon. She led the effort to conserve chip power in microprocessors by selectively using high-performance devices and pioneered the use of error-correcting codes to reduce operating voltages and memories, becoming a principal engineer in 2003. She moved to India in 2005 to manage R&D of mobile platform technologies used for handheld and laptop computers. In 2013, her team was awarded an Intel Achievement Award for contributing to the development of the Minute Intel® architecture core. In her most recent role as vice president in Intel's Platform Engineering Group, she led teams across the United States, Costa Rica, Israel, Malaysia and India charged with developing innovative analog and mixed-signal intellectual property (IP) blocks and IP subsystems for Intel's system-on-chip products. She also managed the emerging technologies group in India developing machine learning and computer vision soft IP.

She earned a bachelor's degree in mathematics, statistics, physics and chemistry and a master's degree in applied mathematics, both from the University of Lucknow in India. She also holds a master's degree in industrial engineering from Oregon State University and a board of director certification from Harvard Business School. She has been issued one U.S. patent and has authored several technical papers on integrated circuit design methodologies and operations research.

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